



ZEGC5MM

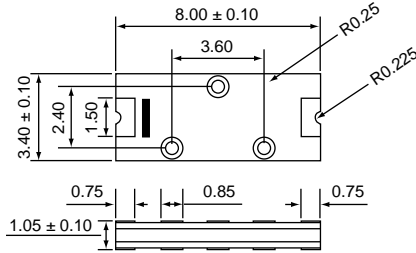
HIGH VOLTAGE SURFACE MOUNT GLASS PASSIVATED JUNCTION HIGH EFFICIENT RECTIFIER

Reverse Voltage - 5000 Volts

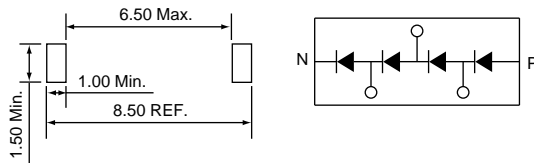
Forward Current - 0.15 Ampere

PATENTED

8034



Mounting PAD



*Dimensions in millimeters

SuperChipTM
SUPEREX IITM



FEATURES

- * Halogen-free type
- * Compliance to RoHS product
- * Leadless chip form, no lead damage
- * Lead-free solder Joint, No Wire bond & Lead Frame
- * Low profile package
- * For surface mounted applications
- * Low power loss, High efficiency
- * High current capability
- * Plastic package has Underwriters Laboratory Flammability Classification 94V-0

MECHANICAL DATA

Case : Packed with FRP substrate and epoxy underfilled

Terminals : Pure Tin plated (Lead-Free),
solderable per MIL-STD-750, Method 2026.

Polarity : Color Cathode band marking

Weight : 0.055 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25 °C ambient temperature unless otherwise specified.	SYMBOLS	ZEGC5MM	UNITS
Maximum repetitive peak reverse voltage	VRRM	5000	Volts
Maximum RMS voltage	VRMS	3500	Volts
Maximum DC blocking voltage	VDC	5000	Volts
Maximum average forward rectified current TL = 120	I(AV)	0.15	Amps
Peak forward surge current 8.3ms single half sine-wave superimposed on rated load	IFSM	5	Amps
Maximum instantaneous forward voltage at IF = 0.15A	VF	16	Volts
Maximum DC reverse current at rated DC blocking voltage TA = 25	IR	8	uA
Maximum reverse recovery time (NOTE 1)	trr	60	nS
Typical junction capacitance (NOTE 2)	CJ	3	pF
Operating junction and storage temperature range	TJ,TSTG	-65 to +175	

NOTES : (1) Reverse recovery test condition : IF = 0.5A, IR = 1.0A, Irr = 0.25A

(2) Measured at 1.0MHz and applied reverse voltage of 4.0 volts

(3) Preliminary draft.

RATINGS AND CHARACTERISTIC CURVES

FIG.1 - FORWARD CURRENT DERATING CURVE

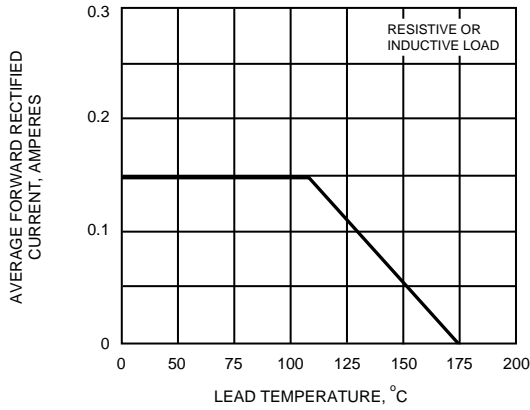


FIG.2 - MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

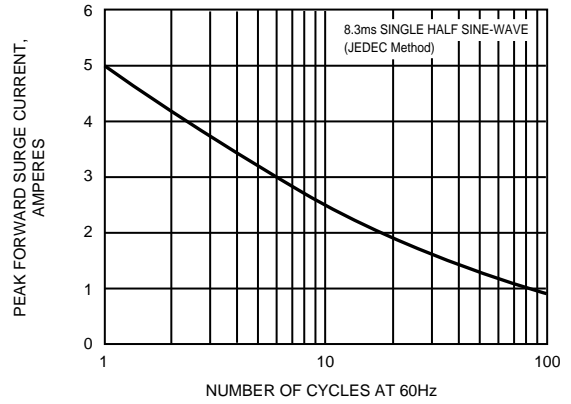


FIG.3 - TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

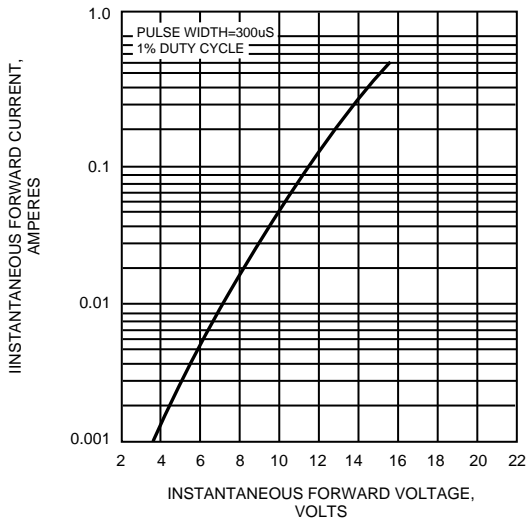


FIG.4 - TYPICAL REVERSE CHARACTERISTICS

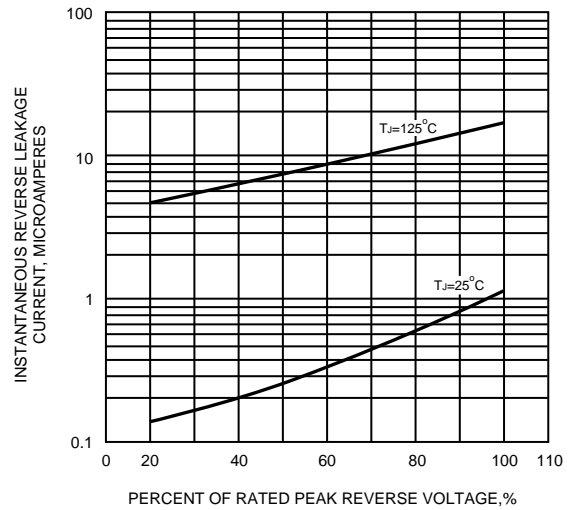


FIG.5 - TYPICAL JUNCTION CAPACITANCE

